Application No.	Applicant(s)	
09/700.359	STEWART ET AL.	
Examiner	Art Unit	
Alecia D. Nelson	2675	
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## **DETAILED ACTION**

## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Jack Schwartz on 09/01/04.

The application has been amended as follows:

In the claims;

Claim 1, line 6, after "and", before "said", insert -each one of--.

## Allowable Subject Matter

- 2. Claims 1-7 are allowed.
- 3. The following is an examiner's statement of reasons for allowance: None of the references used singularly or in combination teaches an arrangement for transferring pixel information with respect to pixels arranged in columns and rows of an array of a display device comprising a plurality of semiconductor switches being separated into groups of semiconductor switches, and each of the groups of semiconductor switches being separated into subgroups of semiconductor switches, wherein a control bus is

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coupled to a first terminal of the semiconductor switches and a plurality of local buses being associated with a respective group of semiconductor switches and having a first bus section extending in a manner to cross the plurality of conductors of the control bus once and a second bus section connected to an end of the first bus section and coupled in a local, clustering bus arrangement to the second terminal of a respective semiconductor switch within each subgroup of the respective group of semiconductor switches as recited in independent claims 1 and 7. Shinya (U.S. patent No. 5,170,158) teaches all that is required, however fails to teach a plurality of semiconductor switches being separated into groups of semiconductor switches and the groups of semiconductor switches being separated into subgroups of semiconductor switches, wherein each conductor of said control bus being associated with a respective subgroup of switches within each group of switches and coupled to the first terminal of each switch within each respective subgroup of semiconductor switches as claimed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alecia D. Nelson whose telephone number is (703) 305-0143. The examiner can normally be reached on Monday-Friday 9:30-6:00. The fax

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phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

adn/ADN September 1, 2004

> AMR A. AWAD PRIMARY EXAMINER

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